

5. *Claim Rejections - 35 USC102*

5 The Examiner rejects Claims 11 to 14 as being anticipated by Chang; Chih-Wei D. et al. (US 5455834 A, hereafter referred to as Chang).

10 In response to the rejection, claims 11 to 14 are amended. The amended claim 11 is directed to a content addressable memory (CAM) device comprising a memory-cell array for storing an array content therein provided for a data-access to an array based on a match with said array content, said CAM device further comprising:

15 an error-check logic circuit connected to said memory-cell array for checking errors of said data access to said memory-cell array wherein said error-check logic circuit further includes an array of identical parity-check circuit blocks represented by  $P[i]$  where  $i=1, 2, 3, \dots, K$  and  $K$  is a positive integer and said identical parity-check circuit blocks performing a parity check on a rotational relationship with each of said  $P[i]$  circuit block receiving input data from a  $P[i-1]$  circuit block and sending output to a  $P[i+1]$  circuit while said  $P[K]$  circuit block sending output data to said  $P[1]$  circuit block.


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25 The CAM device as now directed by the amended claim 11 is different and not anticipated by Chang and would therefore novel and allowable.

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With the amended Specification and claims and the reasons provided above, the Applicant respectfully requests that the rejections be withdrawn and the Patent Application as amended now be allowed.

5      Respectfully submitted  
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